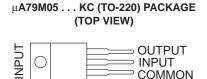
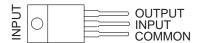
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- 3-Terminal Regulators
- Output Current Up To 500 mA
- No External Components



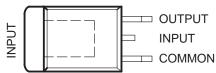
 μ A79M05 . . . KCS (TO-220) PACKAGE (TOP VIEW)



High Power-Dissipation Capability

- Internal Short-Circuit Current Limiting
- Output Transistor Safe-Area Compensation

 $\mu \text{A79M05}, \mu \text{A79M08} \dots \text{KTP PACKAGE}$ (TOP VIEW)



description/ordering information

This series of fixed-negative-voltage integrated-circuit voltage regulators is designed to complement the µA78M00 series in a wide range of applications. These applications include on-card regulation for elimination of noise and distribution problems associated with single-point regulation. Each of these regulators delivers up to 500 mA of output current. The internal current-limiting and thermal-shutdown features of these regulators essentially make them immune to overload. In addition to use as fixed-voltage regulators, these devices can be used with external components to obtain adjustable output voltages and currents, and also as the power-pass element in precision regulators.

ORDERING INFORMATION

ТЈ	V _O (NOM) (V)	PACKAGET		ORDERABLE PART NUMBER	TOP-SIDE MARKING
		PowerFLEX™ (KTP)	Reel of 3000	μΑ79M05CKTPR	μΑ79M05C
000 / 40500	-5	TO-220 (KC)	Tube of 50	μΑ79M05CKC	47014050
0°C to 125°C		TO-220, short shoulder (KCS)	Tube of 20	μΑ79M05CKCS	μΑ79M05C
	-8	PowerFLEX (KTP)	Reel of 3000	μΑ79M08CKTPR	μΑ79M08C

[†] Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.

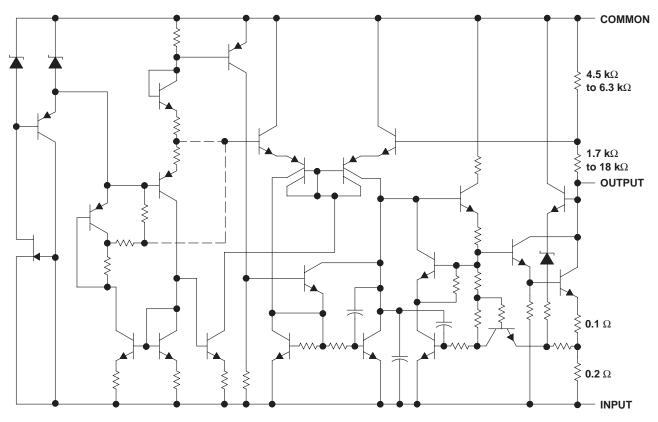


Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

PowerFLEX is a trademark of Texas Instruments.



schematic



Resistor values shown are nominal.

absolute maximum ratings over virtual junction temperature range (unless otherwise noted)

Input voltage, V _I	/
Operating virtual junction temperature, T _J)
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds)
Storage temperature range, T _{stg})

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

package thermal data (see Note 1)

PACKAGE	BOARD	θЈС	θ JA	θ JP ‡
PowerFLEX (KTP)	High K, JESD 51-5	19°C/W	28°C/W	1.4°C/W
TO-220 (KC/KCS)	High K, JESD 51-5	17°C/W	19°C/W	3°C/W

NOTE 1: Maximum power dissipation is a function of TJ(max), θ JA, and TA. The maximum allowable power dissipation at any allowable ambient temperature is $P_D = (T_J(max) - T_A)/\theta_{JA}$. Operating at the absolute maximum T_J of 150°C can affect reliability.



[‡] For packages with exposed thermal pads, such as QFN, PowerPAD, or PowerFLEX, θ, p is defined as the thermal resistance between the die junction and the bottom of the exposed pad.

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recommended operating conditions

			MIN	MAX	UNIT
	land college	μΑ79M05C	-7	-25	V
VI	Input voltage	μΑ79M08C	-10.5	-25	V
lo	Output current			500	mA
TJ	Operating virtual junction temperature		0	125	°C

electrical characteristics at specified virtual junction temperature, V_I = -10 V, I_O = 350 mA, T_J = 25°C (unless otherwise noted)

DADAMETED		μ.					
PARAMETER		MIN	TYP	MAX	UNIT		
Output valta na	V 7V4- 05V	L 5 m A to 250 m A		-4.8	-5	-5.2	
Output voltage	$V_{I} = -7 \text{ V to } -25 \text{ V},$	$I_O = 5 \text{ mA to } 350 \text{ mA}$	$T_J = 0^{\circ}C$ to $125^{\circ}C$	-4.75		-5.25	٧
land callenge as well-flee	$V_1 = -7 \text{ V to } -25 \text{ V}$				7	50	
Input voltage regulation	$V_{I} = -8 \text{ V to } -18 \text{ V}$				3	30	mV
Disale aris of a	$V_1 = -8 \text{ V to } -18 \text{ V},$	I _O = 100 mA,	$T_J = 0^{\circ}C$ to $125^{\circ}C$	50			.ID
Ripple rejection	f = 120 Hz	IO = 300 mA		54	60		dB
Output valtage regulation	$I_O = 5 \text{ mA to } 500 \text{ mA}$				75	100	\/
Output voltage regulation	$I_O = 5$ mA to 350 mA				50		mV
Temperature coefficient of output voltage	I _O = 5 mA,	$T_J = 0$ °C to 125°C			-0.4		mV/°C
Output noise voltage	f = 10 Hz to 100 kHz				125		μV
Dropout voltage					1.1		V
Bias current					1	2	mA
5'	$V_{I} = -8 \text{ V to } -18 \text{ V},$	T _J = 0°C to 125°C				0.4	
Bias current change	$I_{O} = 5 \text{ mA to } 350 \text{ mA},$	$T_J = 0$ °C to 125°C				0.4	mA
Short-circuit output current	V _I = −30 V				140		mA
Peak output current			_		0.65		Α

[†] Pulse-testing techniques maintain T_J as close to T_A as possible. Thermal effects must be taken into account separately. All characteristics are measured with a $2-\mu F$ capacitor across the input and a $1-\mu F$ capacitor across the output.

$\begin{array}{l} \mu \text{A79M00 SERIES} \\ \text{NEGATIVE-VOLTAGE REGULATORS} \end{array}$

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electrical characteristics at specified virtual junction temperature, $V_I = -19 \text{ V}$, $I_O = 350 \text{ mA}$, $T_J = 25^{\circ}\text{C}$ (unless otherwise noted)

24244555		μ Α	μ Α79M08C					
PARAMETER		TEST CONDITIONS†						
Output wells as	V 40.5.V/- 05.V	L 5 A to 050 A		-7.7	-8	-8.3		
Output voltage	$V_I = -10.5 \text{ V to } -25 \text{ V},$	$I_O = 5 \text{ mA to } 350 \text{ mA}$	$T_J = 0^{\circ}C$ to $125^{\circ}C$	-7.6		-8.4	V	
Land calle as as add Car	$V_I = -10.5 \text{ V to } -25 \text{ V}$				8	80	>/	
Input voltage regulation	$V_{I} = -11 \text{ V to } -21 \text{ V}$				4	50	mV	
Disaboutoutou	$V_1 = -11.5 \text{ V to } -21.5 \text{ V},$	$I_{O} = 100 \text{ mA},$	$T_J = 0$ °C to 125°C	50			-ID	
Ripple rejection	f = 120 Hz	IO = 300 mA		54	59		dB	
Output well-many manufaction	I _O = 5 mA to 500 mA				90	160	>/	
Output voltage regulation	I _O = 5 mA to 350 mA			60		mV		
Temperature coefficient of output voltage	I _O = 5 mA,	T _J = 0°C to 125°C			-0.6		mV/°C	
Output noise voltage	f = 10 Hz to 100 kHz				200		μV	
Dropout voltage	I _O = 5 mA				1.1		V	
Bias current					1	2	mA	
5:	$V_I = -10.5 \text{ V to } -25 \text{ V},$	T _J = 0°C to 125°C				0.4		
Bias current change	$I_O = 5 \text{ mA to } 350 \text{ mA},$	$T_J = 0$ °C to 125°C				0.4	mA	
Short-circuit output current	V _I = −30 V				140		mA	
Peak output current					0.65		Α	

[†] Pulse-testing techniques maintain T_J as close to T_A as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 2-μF capacitor across the input and a 1-μF capacitor across the output.







.com 18-Mar-2008

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp (3)
7704001HA	OBSOLETE	CFP	U	10		TBD	Call TI	Call TI
UA79M05CKC	OBSOLETE	TO-220	KC	3		TBD	Call TI	Call TI
UA79M05CKCE3	OBSOLETE	TO-220	KC	3		TBD	Call TI	Call TI
UA79M05CKCS	ACTIVE	TO-220	KCS	3	50	Pb-Free (RoHS)	CU SN	N / A for Pkg Type
UA79M05CKCSE3	ACTIVE	TO-220	KCS	3	50	Pb-Free (RoHS)	CU SN	N / A for Pkg Type
UA79M05CKTPR	OBSOLETE	PFM	KTP	2		TBD	Call TI	Call TI
UA79M05CKTPRG3	OBSOLETE	PFM	KTP	2		TBD	Call TI	Call TI
UA79M05CKVURG3	ACTIVE	PFM	KVU	3	2500	Green (RoHS & no Sb/Br)	CU SN	Level-3-260C-168 HR
UA79M05MUB	OBSOLETE	CFP	U	10		TBD	Call TI	Call TI
UA79M08CKC	OBSOLETE	TO-220	KC	3		TBD	Call TI	Call TI
UA79M08CKTPR	OBSOLETE	PFM	KTP	2		TBD	Call TI	Call TI
UA79M08CKTPRG3	OBSOLETE	PFM	KTP	2		TBD	Call TI	Call TI
UA79M08CKVURG3	ACTIVE	PFM	KVU	3	2500	Green (RoHS & no Sb/Br)	CU SN	Level-3-260C-168 HR

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

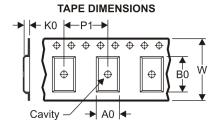
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TAPE AND REEL INFORMATION





A0	Dimension designed to accommodate the component width
В0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

Device	Package Type	Package Drawing			Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
UA79M05CKVURG3	PFM	KVU	3	2500	330.0	16.4	6.9	10.5	2.7	8.0	16.0	Q2
UA79M08CKVURG3	PFM	KVU	3	2500	330.0	16.4	6.9	10.5	2.7	8.0	16.0	Q2



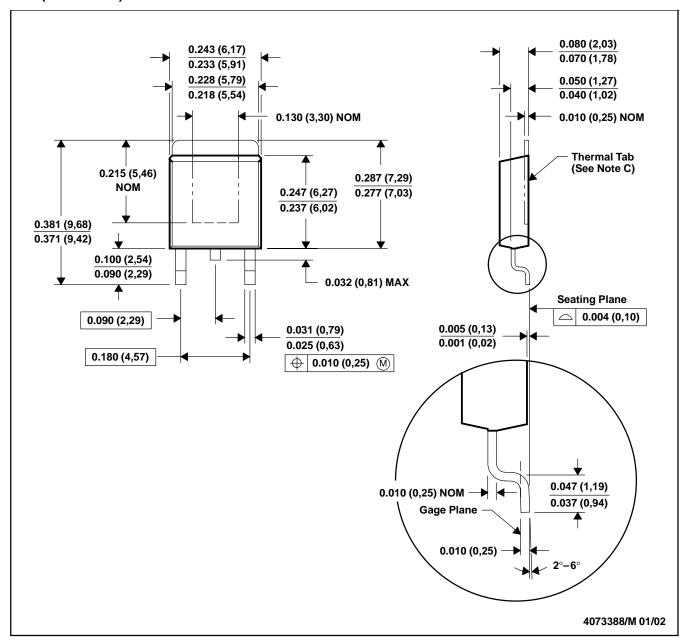


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
UA79M05CKVURG3	PFM	KVU	3	2500	340.0	340.0	38.0
UA79M08CKVURG3	PFM	KVU	3	2500	340.0	340.0	38.0

KTP (R-PSFM-G2)

PowerFLEX™ PLASTIC FLANGE-MOUNT PACKAGE



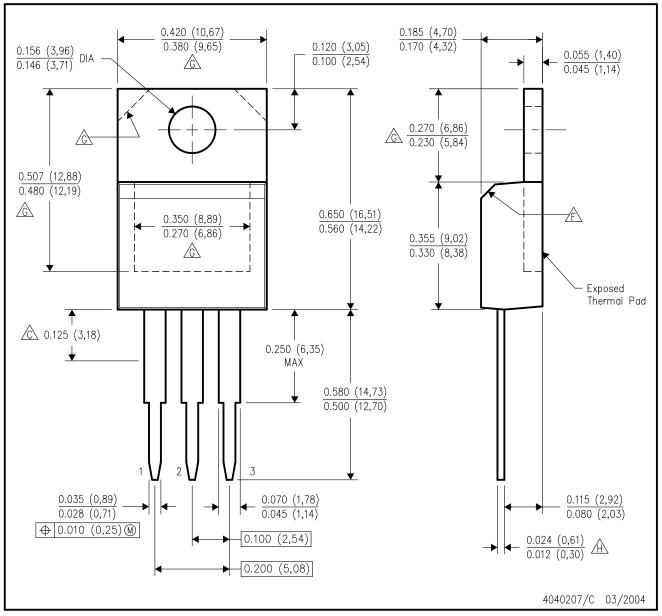
- NOTES: A. All linear dimensions are in inches (millimeters).
 - B. This drawing is subject to change without notice.
 - C. The center lead is in electrical contact with the thermal tab.
 - D. Dimensions do not include mold protrusions, not to exceed 0.006 (0,15).
 - E. Falls within JEDEC TO-252 variation AC.

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KC (R-PSFM-T3)

PLASTIC FLANGE-MOUNT PACKAGE



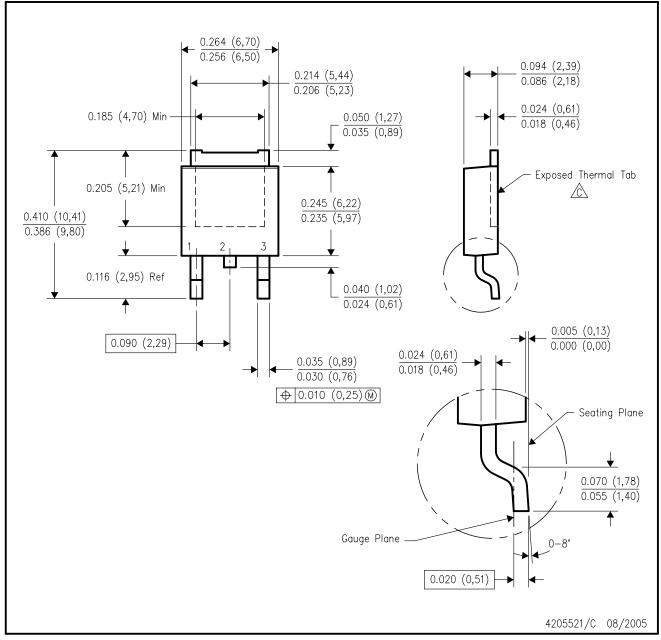
NOTES: A

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Lead dimensions are not controlled within this area.
- D. All lead dimensions apply before solder dip.
- E. The center lead is in electrical contact with the mounting tab.
- The chamfer is optional.
- Thermal pad contour optional within these dimensions.
- ⚠ Falls within JEDEC TO—220 variation AB, except minimum lead thickness.



KVU (R-PSFM-G3)

PLASTIC FLANGE-MOUNT PACKAGE



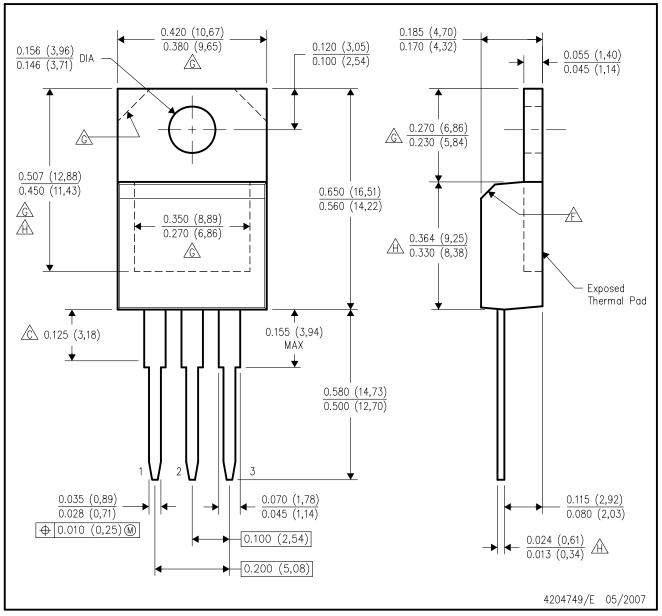
NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- The center lead is in electrical contact with the exposed thermal tab.
- D. Body Dimensions do not include mold flash or protrusions. Mold flash and protrusion shall not exceed 0.006 (0,15) per side.
- E. Falls within JEDEC TO-252 variation AA.



KCS (R-PSFM-T3)

PLASTIC FLANGE-MOUNT PACKAGE



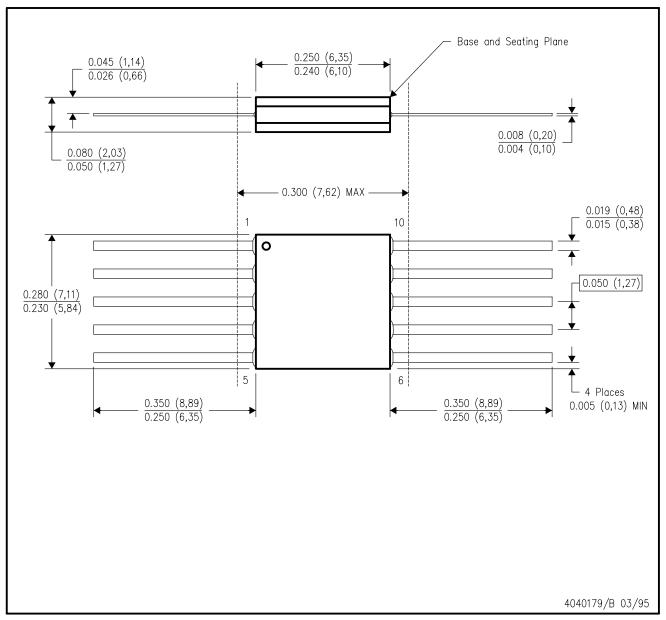
NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Lead dimensions are not controlled within this area.
- D. All lead dimensions apply before solder dip.
- E. The center lead is in electrical contact with the mounting tab.
- The chamfer is optional.
- Thermal pad contour optional within these dimensions.
- Falls within JEDEC TO-220 variation AB, except minimum lead thickness, minimum exposed pad length, and maximum body length.



U (S-GDFP-F10)

CERAMIC DUAL FLATPACK



NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only.
- E. Falls within MIL STD 1835 GDFP1-F10 and JEDEC MO-092AA



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